

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	7510	data with stripe	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/11/26 18:45
L2	483	logic adj engine	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/11/26 18:46
L3	1843	cache with bypass\$3	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/11/26 18:47
L4	2308	storage adj processor	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/11/26 18:47
L5	474	3 same first	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/11/26 18:47
L6	0	3 same (first adj operation)	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/11/26 18:48
L7	55	3 same (first with operation)	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/11/26 18:48
L8	1	1 and 7	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/11/26 18:48
L9	1	2 and 7	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/11/26 18:48
L10	2383	(711/114).CCLS.	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/11/26 18:48

## EAST Search History

L11	231	(711/155).CCLS.	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/11/26 18:48
L12	985	(711/156).CCLS.	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/11/26 18:49
L13	1	(transfer\$6 and (non-volatile or nonvolatile) and (logic adj engine) and stripe and cache and bypass\$5).clm.	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/11/26 18:50